	Application No.	Applicant(s)
Notice of Allowability	10/718,063	NGO
	Examiner	Art Unit
	Vit W. Miska	2841
The MAILING DATE of this communication app All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85 NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT I of the Office or upon petition by the applicant. See 37 CFR 1.31	S (OR REMAINS) CLOSED  i) or other appropriate comm  RIGHTS. This application is	in this application. If not included nunication will be mailed in due course. THIS
1. This communication is responsive to		
2. ☑ The allowed claim(s) is/are <u>1-20</u> .		
3. The drawings filed on 20 November 2003 are accepted b	y the Examiner.	
<ul> <li>4. ☐ Acknowledgment is made of a claim for foreign priority of a) ☐ All b) ☐ Some* c) ☐ None of the:</li> <li>1. ☐ Certified copies of the priority documents have 2. ☐ Certified copies of the priority documents have 3. ☐ Copies of the certified copies of the priority documents have a copies of the priority documents have a copies of the priority documents have a copies of the certified copies of the priority documents have a copies of the priority documents have a copies of the certified copies of the priority documents have a copies of the priority</li></ul>	re been received. re been received in Applicati	on No
Applicant has THREE MONTHS FROM THE "MAILING DATE noted below. Failure to timely comply will result in ABANDON THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.	of this communication to file MENT of this application.	e a reply complying with the requirements
5. A SUBSTITUTE OATH OR DECLARATION must be subr INFORMAL PATENT APPLICATION (PTO-152) which give	nitted. Note the attached EX	AMINER'S AMENDMENT or NOTICE OF or declaration is deficient.
6. ☐ CORRECTED DRAWINGS ( as "replacement sheets") mu (a) ☐ including changes required by the Notice of Draftsper		w ( PTO-948) attached
1)  hereto or 2)  to Paper No./Mail Date		(
(b) ☐ including changes required by the attached Examiner Paper No./Mail Date	's Amendment / Comment o	r in the Office action of
Identifying indicia such as the application number (see 37 CFR each sheet. Replacement sheet(s) should be labeled as such in	1.84(c)) should be written on t the header according to 37 Cl	he drawings in the front (not the back) of FR 1.121(d).
7. DEPOSIT OF and/or INFORMATION about the deposit attached Examiner's comment regarding REQUIREMENT	osit of BIOLOGICAL MAT FOR THE DEPOSIT OF BI	ERIAL must be submitted. Note the OLOGICAL MATERIAL.
Attachment(s)  1. ☑ Notice of References Cited (PTO-892)  2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)		formal Patent Application (PTO-152)
•	Paner No.	ummary (PTO-413), /Mail Date
<ul> <li>Information Disclosure Statements (PTO-1449 or PTO/SB/Paper No./Mail Date 11/20/2003</li> <li>Examiner's Comment Regarding Requirement for Deposit</li> </ul>	08), 7. ⊠ Examiner's	Amendment/Comment  Statement of Reasons for Allowance
of Biological Material	9.  Other	1 1 1 1
		Primary Examiner

Art Unit: 2841

## **EXAMINER'S AMENDMENT**

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

The application has been amended as follows:

In the specification, page 4, after the last line add:

"Fig. 10 is a block diagram of another embodiment of the invention."

The added material has been added to provide a brief description of the figure.

The following is an examiner's statement of reasons for allowance:

Claims 1-9 are allowed because the prior art does not disclose or suggest a method or circuit for generating a non-integer frequency divided clocks signal including circuitry for outputting a present value M of an integer index in response to a logic transition of a shift clock signal, glitch free selector circuitry for selecting one of K output signal phases P(M) of a multiple phase clock as a clock output signal in response to the present value M of the integer index, synchronous divide by N counter clocked by the clock output signal to generate a frequency divided clock signal and the shift clock signal, circuitry in the divide by N counter for determining when the latter has been clocked N times by the clock output signal, circuitry for receiving an integer fractional divisor of value S less than (K-1) and modulo (K-1) adding circuitry for adding the value S to the integer index. Claims 10-15 is allowed because the claims set forth a phase locked loop including the circuit for generating a non-integer frequency divided clock signal of claim 4. Claims 16-20 are allowed because these claims set forth a data processing system including the circuitry of claim 4.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Vit W. Miska whose telephone number is 571-272-2108. The examiner can normally be reached on M-F 9-5:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, K. Cuneo can be reached on 571-272-1957. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

VM 3/3/2005

> Vit Miska Primary Examiner